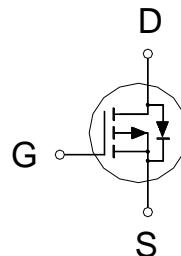


P-Channel Logic Level Enhancement Mode Field Effect Transistor

Product Summary:

BV <sub>DSS</sub>	-20V
R <sub>DSON</sub> (MAX.)	44mΩ
I <sub>D</sub>	-12A



Pb-Free Lead Plating & Halogen Free



**ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25 °C Unless Otherwise Noted)**

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNIT
Gate-Source Voltage		V <sub>GS</sub>	±12	V
Continuous Drain Current	T <sub>C</sub> = 25 °C	I <sub>D</sub>	-12	A
	T <sub>A</sub> = 25 °C		-6	
	T <sub>C</sub> = 100 °C		-8	
Pulsed Drain Current <sup>1</sup>		I <sub>DM</sub>	-48	
Power Dissipation	T <sub>C</sub> = 25 °C	P <sub>D</sub>	21	W
	T <sub>C</sub> = 100 °C		8.3	
Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2.5	W
	T <sub>A</sub> = 70 °C		1.6	
Operating Junction & Storage Temperature Range		T <sub>j</sub> , T <sub>stg</sub>	-55 to 150	°C

**THERMAL RESISTANCE RATINGS**

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNIT
Junction-to-Case	R <sub>θJC</sub>	6	50	°C / W
Junction-to-Ambient <sup>3</sup>	R <sub>θJA</sub>			

<sup>1</sup>Pulse width limited by maximum junction temperature.

<sup>2</sup>Duty cycle ≤ 1%

<sup>3</sup>50°C / W when mounted on a 1 in<sup>2</sup> pad of 2 oz copper.

ELECTRICAL CHARACTERISTICS ( $T_j = 25^\circ\text{C}$ , Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0V, I_D = -250\mu\text{A}$	-20			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$	-0.3	-0.75	-1.2	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0V, V_{GS} = \pm 12V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -16V, V_{GS} = 0V$			-1	$\mu\text{A}$
		$V_{DS} = -16V, V_{GS} = 0V, T_j = 125^\circ\text{C}$			-10	
On-State Drain Current <sup>1</sup>	$I_{D(\text{ON})}$	$V_{DS} = -5V, V_{GS} = -4.5V$	-12			A
Drain-Source On-State Resistance <sup>1</sup>	$R_{DS(\text{ON})}$	$V_{GS} = -4.5V, I_D = -5\text{A}$		37	44	$\text{m}\Omega$
		$V_{GS} = -2.5V, I_D = -4\text{A}$		55	70	
		$V_{GS} = -1.8V, I_D = -1\text{A}$		65	90	
Forward Transconductance <sup>1</sup>	$g_{fs}$	$V_{DS} = -5V, I_D = -4\text{A}$		14		S
DYNAMIC						
Input Capacitance	$C_{iss}$	$V_{GS} = 0V, V_{DS} = -10V, f = 1\text{MHz}$		679		$\text{pF}$
Output Capacitance	$C_{oss}$			124		
Reverse Transfer Capacitance	$C_{rss}$			106		
Total Gate Charge <sup>1,2</sup>	$Q_g$	$V_{DS} = -10V, V_{GS} = -4.5V, I_D = -4\text{A}$		12.8		$\text{nC}$
Gate-Source Charge <sup>1,2</sup>	$Q_{gs}$			2.2		
Gate-Drain Charge <sup>1,2</sup>	$Q_{gd}$			4.1		
Turn-On Delay Time <sup>1,2</sup>	$t_{d(on)}$	$V_{DS} = -10V, I_D = -1\text{A}, V_{GS} = -4.5V, R_{GS} = 6\Omega$		10		$\text{ns}$
Rise Time <sup>1,2</sup>	$t_r$			18		
Turn-Off Delay Time <sup>1,2</sup>	$t_{d(off)}$			32		
Fall Time <sup>1,2</sup>	$t_f$			22		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ( $T_c = 25^\circ\text{C}$ )						
Continuous Current	$I_s$				-12	A
Pulsed Current <sup>3</sup>	$I_{SM}$				-48	
Forward Voltage <sup>1</sup>	$V_{SD}$	$I_F = -5\text{A}, V_{GS} = 0V$			-1.2	V

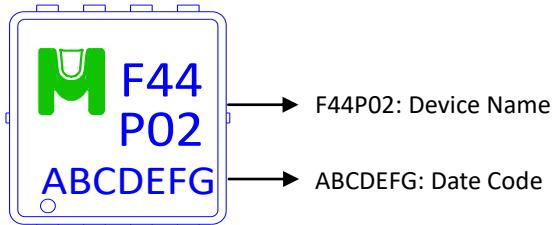
<sup>1</sup>Pulse test : Pulse Width  $\leq 300 \mu\text{sec}$ , Duty Cycle  $\leq 2\%$ .

<sup>2</sup>Independent of operating temperature.

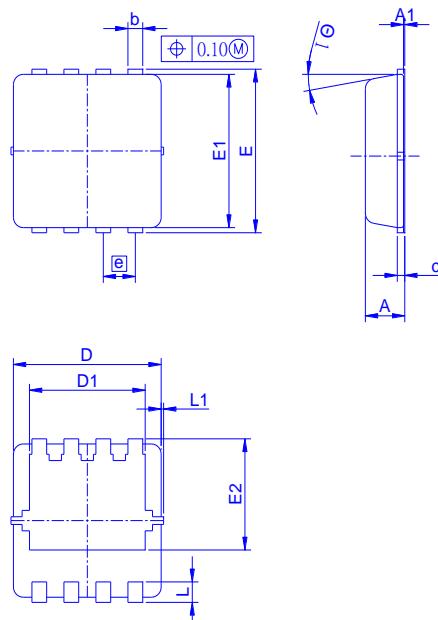
<sup>3</sup>Pulse width limited by maximum junction temperature.

### Ordering & Marking Information:

Device Name: EMF44P02V for EDFN 3 x 3



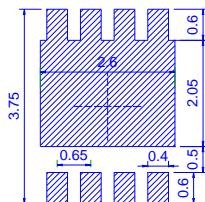
### Outline Drawing



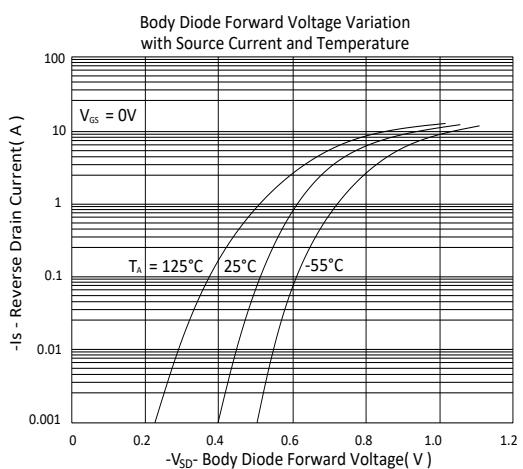
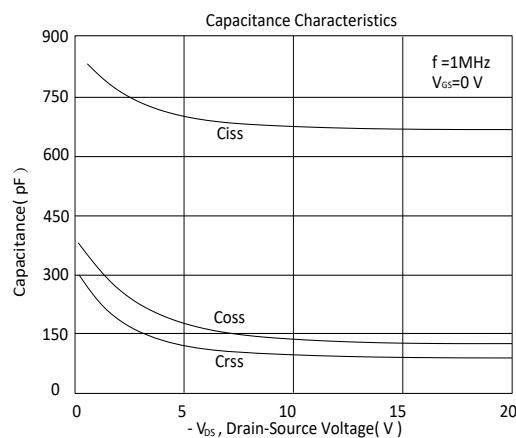
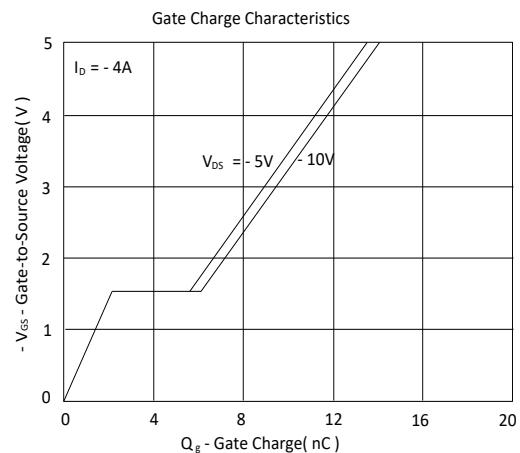
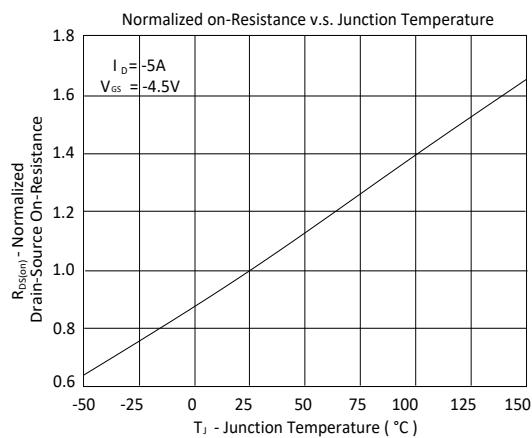
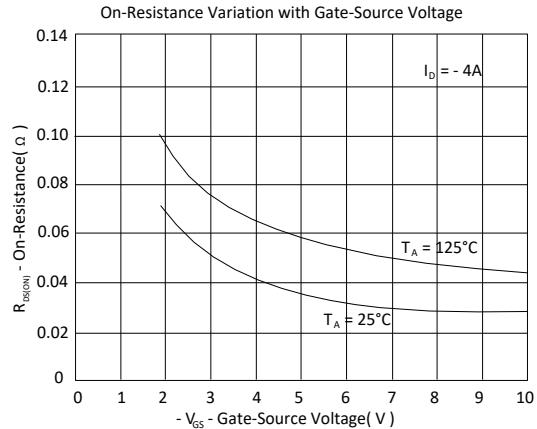
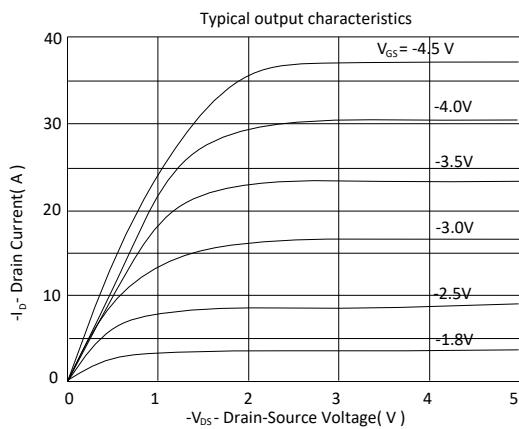
Dimension in mm

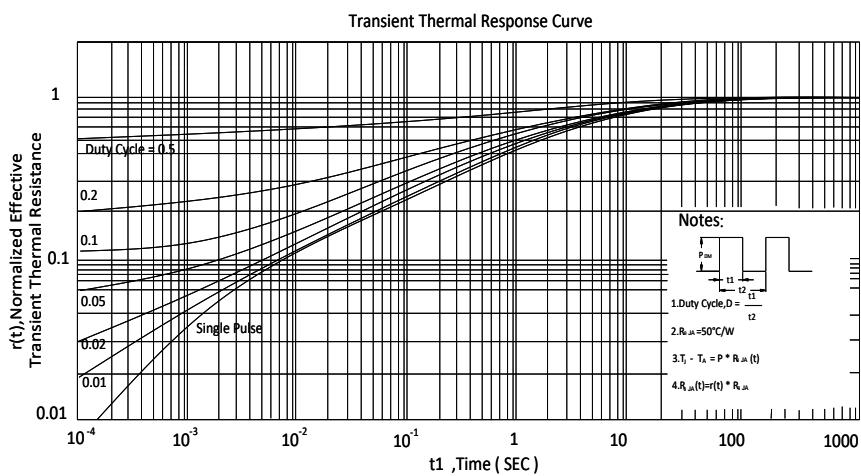
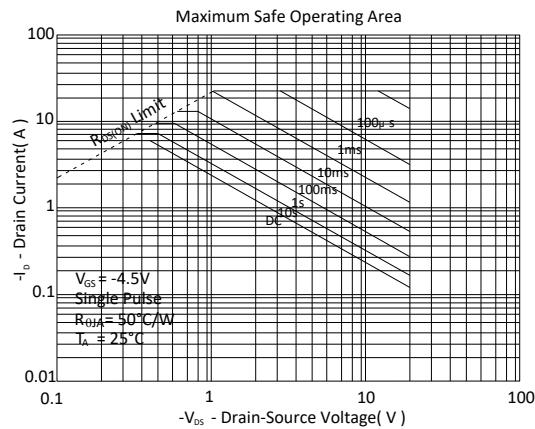
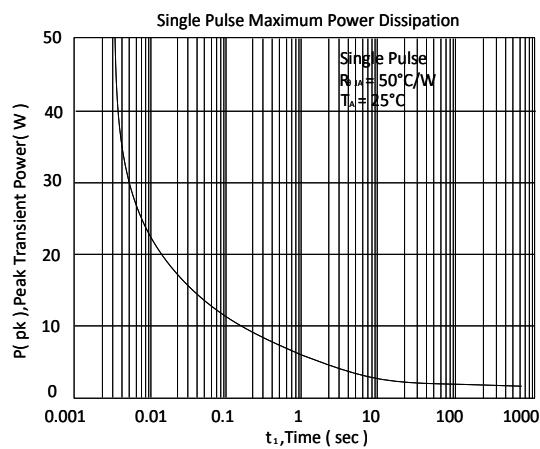
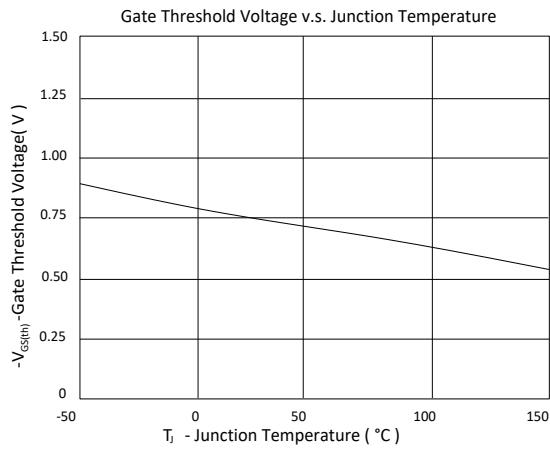
Dimension	A	A1	b	c	D	D1	E	E1	E2	e	L	L1	$\theta 1$
Min.	0.65	0	0.20	0.10	2.90	2.15	3.10	2.90	1.53	0.55	0.25	-	0°
Typ.	0.75	-	0.30	0.15	3.00	2.45	3.20	3.00	1.97	0.65	0.40	0.075	10°
Max.	0.90	0.05	0.40	0.25	3.30	2.74	3.50	3.30	2.59	0.75	0.60	0.150	14°

### Recommended minimum pads



### TYPICAL CHARACTERISTICS





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CLICK TO CONTINUE

View additional material information including performance and processing data

Component - Plastics

Guide Information

The information presented on the UL Prospector datasheet was acquired by UL Prospector from the producer of the material. UL Prospector makes substantial efforts to assure the accuracy of this data. However, UL Prospector assumes no responsibility for the data values and strongly encourages that upon final material selection, data points are validated with the material supplier.

E41429

**SUMITOMO BAKELITE CO LTD**

5-8 HIGASHI-SHINAGAWA 2-CHOME, SHINAGAWA-KU TOKYO 140-0002 JP

**EME-G600, EME-G600C**

Epoxy Molding Resin (EP - Molding) "SUMIKON", furnished as tablets

Color	Min. Thk (mm)	Flame Class	HWI	HAI	RTI Elec	RTI Imp	RTI Str
BK	3.0	V-0	0	0	130	130	130

Comparative Tracking Index (CTI): 1

Dielectric Strength (kV/mm): 24

High-Voltage Arc Tracking Rate (HVTR): -

Dimensional Change (%): -

Inclined Plane Tracking (IPT) kV: -

Volume Resistivity (10<sup>12</sup> ohm-cm): -

Surface Resistivity (10<sup>12</sup> ohms/square): -

High Volt, Low Current Arc Resis (D495): -

ANSI/UL 94 small-scale test data does not pertain to building materials, furnishings and related contents. ANSI/UL 94 small-scale test data is intended solely for determining the flammability of plastic materials used in the components and parts of end-product devices and appliances, where the acceptability of the combination is determined by UL.

Report Date: 2002-07-11

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Last Revised: 2020-07-29



**IEC and ISO Test Methods**

Test Name	Test Method	Units	Thk (mm)	Value
Flammability	IEC 60695-11-10	Class (color)	3.0	V-0 (BK)
Glow-Wire Flammability (GWF)	IEC 60695-2-12	°C	-	-
Glow-Wire Ignition (GWIT)	IEC 60695-2-13	°C	-	-
IEC Comparative Tracking Index	IEC 60112	Volts (Max)	-	-
IEC Ball Pressure	IEC 60695-10-2	°C	-	-
ISO Heat Deflection (1.80 MPa)	ISO 75-2	°C	-	-
ISO Tensile Strength	ISO 527-2	MPa	-	-
ISO Flexural Strength	ISO 178	MPa	-	-
ISO Tensile Impact	ISO 8256	kJ/m <sup>2</sup>	-	-
ISO Izod Impact	ISO 180	kJ/m <sup>2</sup>	-	-
ISO Charpy Impact	ISO 179-1	kJ/m <sup>2</sup>	-	-