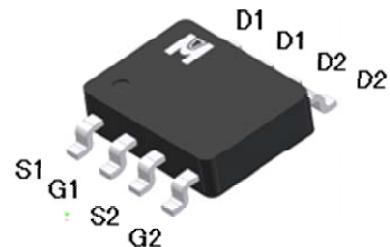
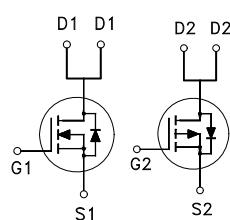


N & P-Channel Logic Level Enhancement Mode Field Effect Transistor

Product Summary:

	N-CH	P-CH
BV _{DSS}	30V	-30V
R _{DSON} (MAX.)	28mΩ	45mΩ
I _D	7A	-6A



Pb-Free Lead Plating & Halogen Free



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS		UNIT
Gate-Source Voltage		V_{GS}	N-CH	P-CH	V
			±20	±20	
Continuous Drain Current	$T_A = 25^\circ\text{C}$	I_D	7	-6	A
	$T_A = 100^\circ\text{C}$		6	-5	
Pulsed Drain Current ¹		I_{DM}	28	-24	
Power Dissipation	$T_A = 25^\circ\text{C}$	P_D	2		W
	$T_A = 100^\circ\text{C}$		0.8		
Operating Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 150		°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNIT
Junction-to-Case	$R_{\theta JC}$		25	°C / W
Junction-to-Ambient ³	$R_{\theta JA}$		62.5	

¹Pulse width limited by maximum junction temperature.

²Duty cycle ≤ 1%

³62.5°C / W when mounted on a 1 in² pad of 2 oz copper.

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT	
			MIN	TYP	MAX		
STATIC							
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	N-CH	30		V	
		$V_{\text{GS}} = 0\text{V}, I_D = -250\mu\text{A}$	P-CH	-30			
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	N-CH	1.0	1.5	3.0	
		$V_{\text{DS}} = V_{\text{GS}}, I_D = -250\mu\text{A}$	P-CH	-1.0	-1.5	-3.0	
Gate-Body Leakage	I_{GSS}	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$	N-CH			± 100	
		$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$	P-CH			± 100	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 24\text{V}, V_{\text{GS}} = 0\text{V}$	N-CH			1	
		$V_{\text{DS}} = -24\text{V}, V_{\text{GS}} = 0\text{V}$	P-CH			-1	
		$V_{\text{DS}} = 20\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 125^\circ\text{C}$	N-CH			25	
		$V_{\text{DS}} = -20\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 125^\circ\text{C}$	P-CH			-25	
On-State Drain Current ¹	$I_{\text{D}(\text{ON})}$	$V_{\text{DS}} = 5\text{V}, V_{\text{GS}} = 10\text{V}$	N-CH	7		A	
		$V_{\text{DS}} = -5\text{V}, V_{\text{GS}} = -10\text{V}$	P-CH	-6			
Drain-Source On-State Resistance ¹	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 10\text{V}, I_D = 7\text{A}$	N-CH		24	28	
		$V_{\text{GS}} = -10\text{V}, I_D = -6\text{A}$	P-CH		39	45	
		$V_{\text{GS}} = 4.5\text{V}, I_D = 6\text{A}$	N-CH		33	40	
		$V_{\text{GS}} = -4.5\text{V}, I_D = -5\text{A}$	P-CH		56	72	
Forward Transconductance ¹	g_{fs}	$V_{\text{DS}} = 5\text{V}, I_D = 7\text{A}$	N-CH		14	S	
		$V_{\text{DS}} = -5\text{V}, I_D = -6\text{A}$	P-CH		16		
DYNAMIC							
Input Capacitance	C_{iss}	<p style="text-align: center;">N-CH $V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 15\text{V}, f = 1\text{MHz}$ P-CH $V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = -15\text{V}, f = 1\text{MHz}$</p>	N-CH		520	pF	
			P-CH		820		
Output Capacitance	C_{oss}		N-CH		88		
			P-CH		122		
Reverse Transfer Capacitance	C_{rss}		N-CH		62		
			P-CH		97		

Total Gate Charge ^{1,2}	Q_g	N-CH $V_{DS} = 15V, V_{GS} = 10V,$ $I_D = 7A$ P-CH $V_{DS} = -15V, V_{GS} = -10V,$ $I_D = -6A$	N-CH		11.5		nC
Gate-Source Charge ^{1,2}	Q_{gs}		P-CH		9		
Gate-Drain Charge ^{1,2}	Q_{gd}		N-CH		1.6		
			P-CH		2.2		
			N-CH		2.8		
			P-CH		2.5		
Turn-On Delay Time ^{1,2}	$t_{d(on)}$	N-CH $V_{DS} = 10V,$ $I_D = 1A, V_{GS} = 10V, R_{GS} = 6\Omega$ P-CH $V_{DS} = -10V,$ $I_D = -1A, V_{GS} = -10V, R_{GS} = 6\Omega$	N-CH		11		nS
Rise Time ^{1,2}	t_r		P-CH		10		
Turn-Off Delay Time ^{1,2}	$t_{d(off)}$		N-CH		16		
Fall Time ^{1,2}	t_f		P-CH		15		
			N-CH		36		
			P-CH		28		
			N-CH		20		
			P-CH		15		

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_c = 25^\circ C$)

Continuous Current	I_S	$I_F = I_S, V_{GS} = 0V$	N-CH			2.3	A	
Pulsed Current ³	I_{SM}		P-CH			-2.3		
Forward Voltage ¹	V_{SD}		N-CH			9.2	V	
			P-CH			-9.2		

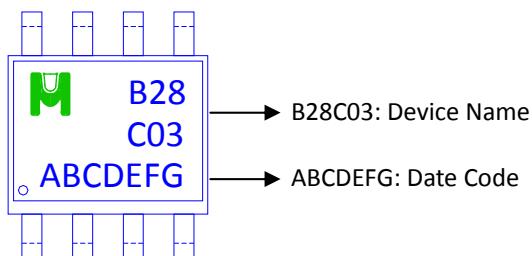
¹Pulse test : Pulse Width $\leq 300 \mu\text{sec}$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

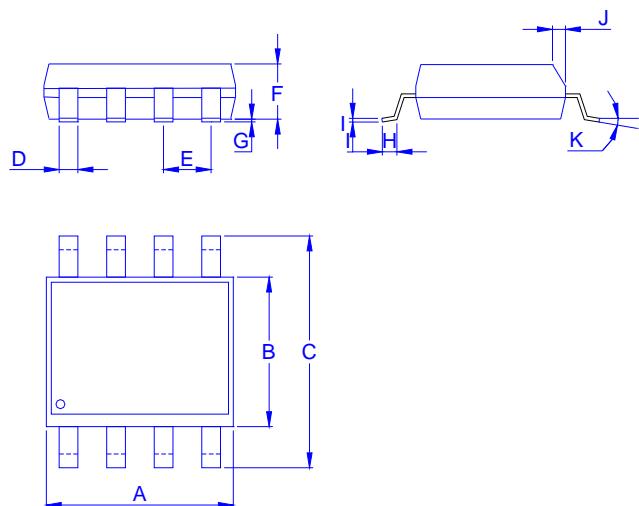
³Pulse width limited by maximum junction temperature.

Ordering & Marking Information:

Device Name: EMB28C03G for SOP-8



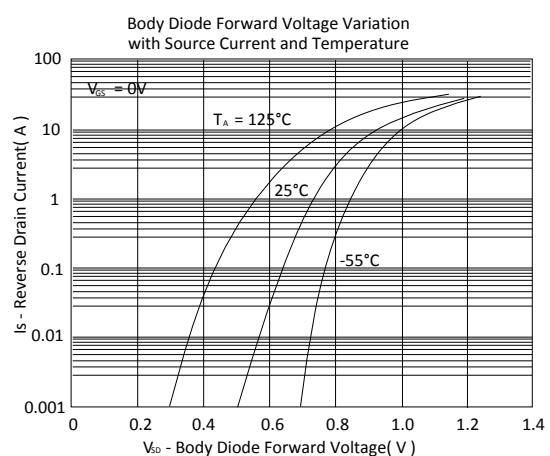
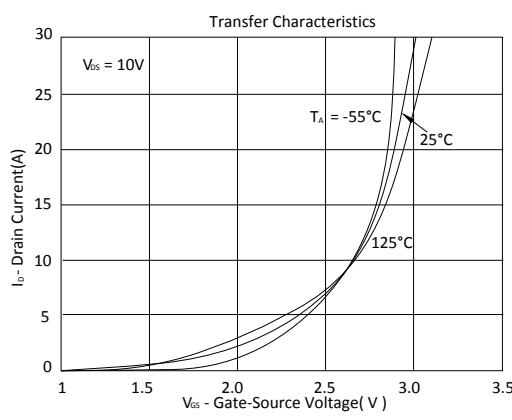
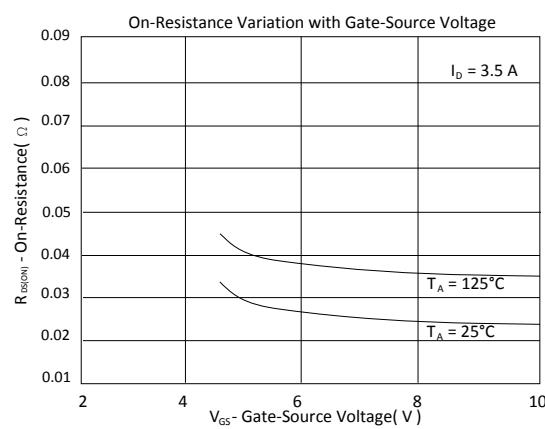
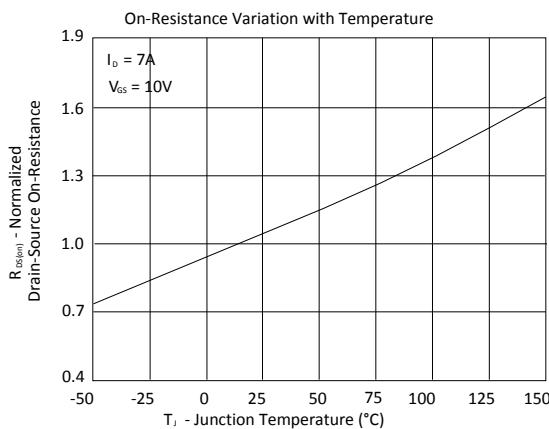
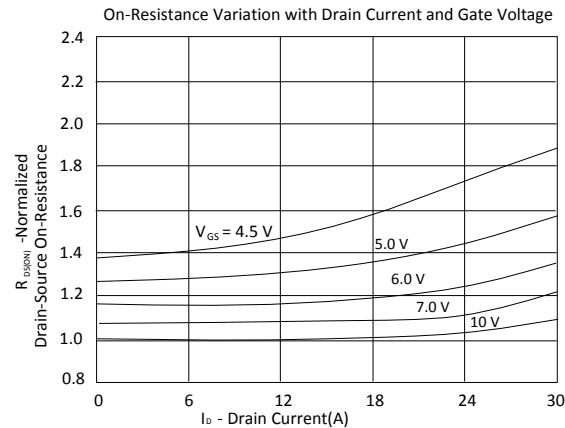
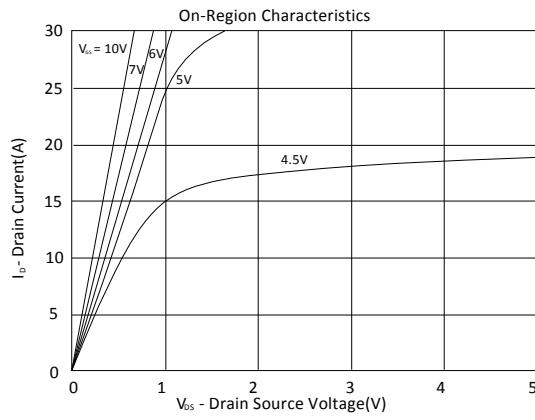
Outline Drawing

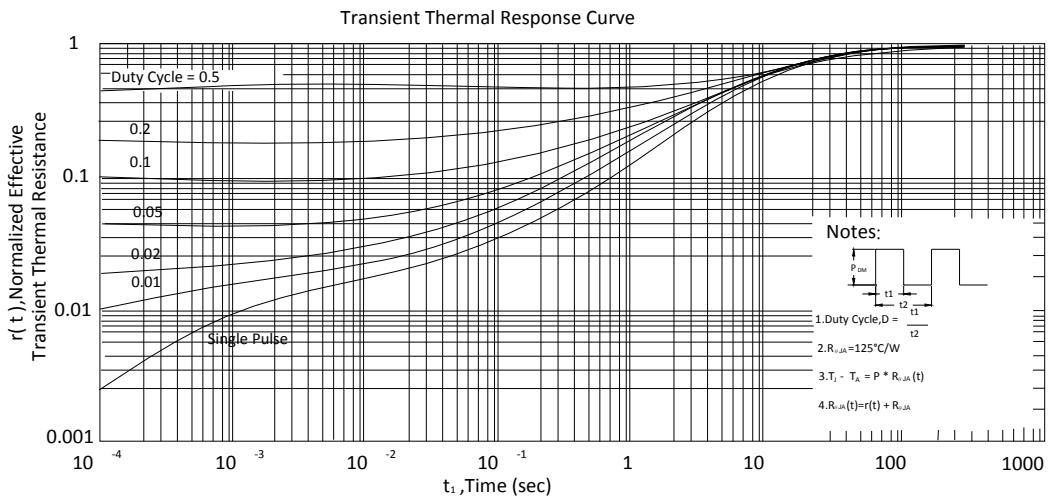
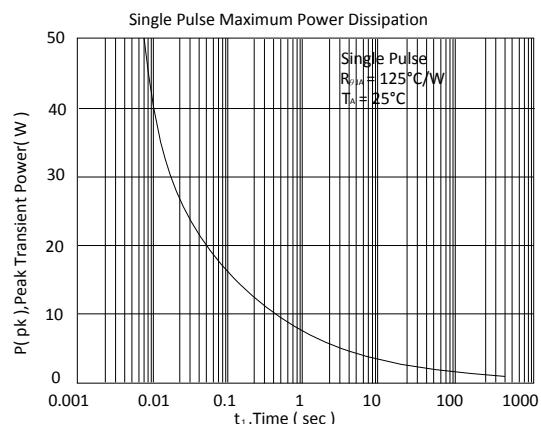
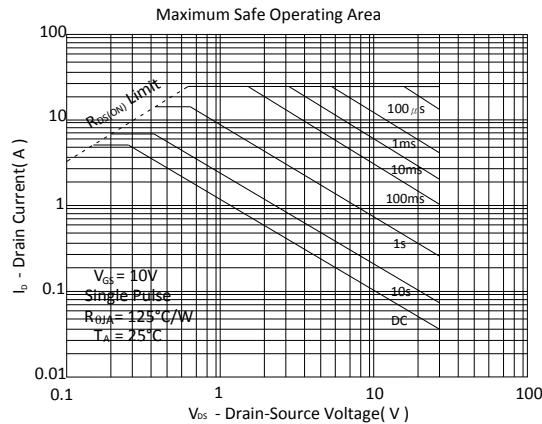
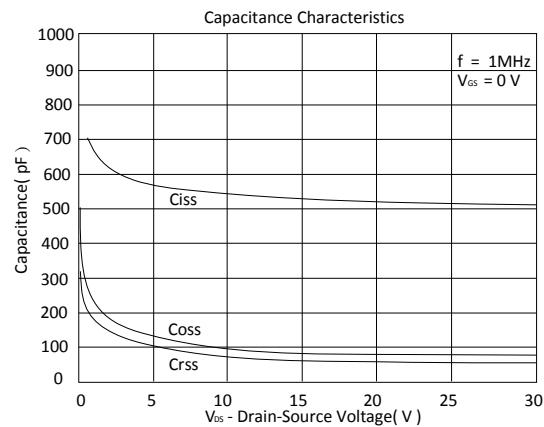
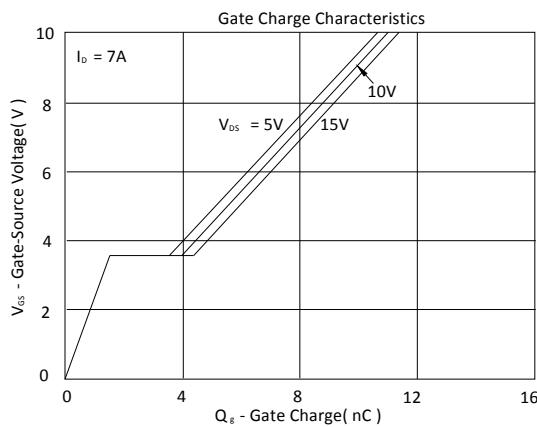


Dimension in mm

Dimension	A	B	C	D	E	F	G	H	I	J	K
Min.	4.70	3.70	5.80	0.33		1.20	0.08	0.40	0.19	0.25	0°
Typ.					1.27						
Max.	5.10	4.10	6.20	0.51		1.62	0.28	0.83	0.26	0.50	8°

N-Channel







P-Channel

