

N-Channel + P-Channel Logic Level Enhancement Mode Field Effect Transistor

•Product Summary:

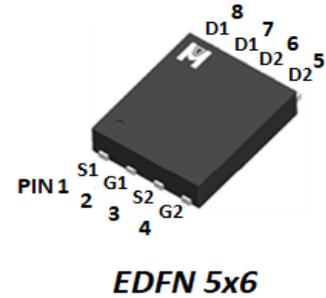
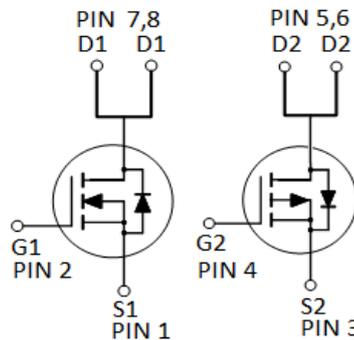
	N-CH	P-CH
BV_{DSS}	30V	-30V
$R_{DSON (MAX.)}@V_{GS}=10V$	18m Ω	20m Ω
$R_{DSON (MAX.)}@V_{GS}=4.5V$	26m Ω	30m Ω
$I_D @T_C=25^\circ C$	29A	-44A

N + P Channel MOSFET

UIS, Rg 100% Tested

RoHS & Halogen Free & TSCA Compliant

• Pin Description:



EDNF 5x6



• ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS		UNIT	
		N-CH	P-CH		
Gate-Source Voltage	V_{GS}	± 20	± 20	V	
Continuous Drain Current	I_D	$T_C = 25^\circ C$	29	-44	A
		$T_C = 100^\circ C$	18	-28	
Continuous Drain Current	I_D	$T_A = 25^\circ C$	7	-8	
		$T_A = 70^\circ C$	6	-6	
Pulsed Drain Current ¹	I_{DM}	53	-59		
Avalanche Current	I_{AS}	31	-38		
Avalanche Energy	E_{AS}	48	72	mJ	
Repetitive Avalanche Energy ²	E_{AR}	24	36		
Power Dissipation	P_D	$T_C = 25^\circ C$	31	63	W
		$T_C = 100^\circ C$	13	25	
Power Dissipation	P_D	$T_A = 25^\circ C$	2.1	2.2	W
		$T_A = 70^\circ C$	1.4	1.4	
Operating Junction & Storage Temperature Range	T_J, T_{stg}	-55 to 150		$^\circ C$	

• 100% UIS testing in condition of $V_D=25V, L=0.1mH, V_G=10V, I_L=19A, R_G=25\Omega$, Rated $V_{DS}=30V$ N-CH

• 100% UIS testing in condition of $V_D=25V, L=0.1mH, V_G=10V, I_L=23A, R_G=25\Omega$, Rated $V_{DS}=30V$ P-CH

•THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM		UNIT
			N-CH	P-CH	
Junction-to-Case	$R_{\theta JC}$		4	2	$^\circ C/W$
Junction-to-Ambient ^{3,4}		$t \leq 10s$	26	25	
		Steady-State	59	58	

¹Pulse width limited by maximum junction temperature.

²Duty cycle < 1%

³The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ C$.

⁴Guarantee by Engineering test



▪ N-CH_ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage ⁴	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	30			V
Gate Threshold Voltage ⁴	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1.2	1.6	2.5	
Gate-Body Leakage ⁴	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V			±100	nA
Zero Gate Voltage Drain Current ⁴	I _{DSS}	V _{DS} = 30V, V _{GS} = 0V			1	μA
		V _{DS} = 30V, V _{GS} = 0V, T _J = 125 °C			25	
On-State Drain Current ¹	I _{D(ON)}	V _{DS} = 10V, V _{GS} = 10V	29			A
Drain-Source On-State Resistance ^{1,4}	R _{DS(ON)}	V _{GS} = 10V, I _D = 20A		12	18	mΩ
		V _{GS} = 4.5V, I _D = 15A		17	26	
Forward Transconductance ¹	g _{fs}	V _{DS} = 5V, I _D = 12A		32		S
DYNAMIC						
Input Capacitance ⁵	C _{iss}	V _{GS} = 0V, V _{DS} = 15V, f = 1MHz		730		pF
Output Capacitance ⁵	C _{oss}			122		
Reverse Transfer Capacitance ⁵	C _{rss}			105		
Gate Resistance ^{4,5}	R _g	f = 1MHz		1.4		Ω
Total Gate Charge ^{1,2,5}	Q _g (V _{GS} =10V)	V _{DS} = 15V, V _{GS} = 10V, I _D = 20A		17		nC
Total Gate Charge ^{1,2,5}	Q _g (V _{GS} =4.5V)			7.9		
Gate-Source Charge ^{1,2,5}	Q _{gs}			3.7		
Gate-Drain Charge ^{1,2,5}	Q _{gd}			4.7		
Turn-On Delay Time ^{1,2,5}	t _{d(on)}	V _{DS} = 15V, V _{GS} = 10V, I _D = 5A, R _g = 3Ω		6.4		nS
Rise Time ^{1,2,5}	t _r			12		
Turn-Off Delay Time ^{1,2,5}	t _{d(off)}			17		
Fall Time ^{1,2,5}	t _f			6.1		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS						
Continuous Current	I _S				26	A
Pulsed Current ³	I _{SM}				53	
Forward Voltage ^{1,4}	V _{SD}	I _F = 20A, V _{GS} = 0V			1.2	V
Reverse Recovery Time ⁵	t _{rr}	I _F = 20A, dI _F /dt = 100A / μS		8.7		nS
Peak Reverse Recovery Current ⁵	I _{RM(REC)}			0.7		A
Reverse Recovery Charge ⁵	Q _{rr}			3.2		nC

¹Pulse test : Pulse Width ≤ 300 usec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

³Pulse width limited by maximum junction temperature.

⁴Guarantee by FT test Item

⁵Guarantee by Engineering test

EMC will review datasheet by quarter, and update new version.



▪ N-CH_TYPICAL CHARACTERISTICS

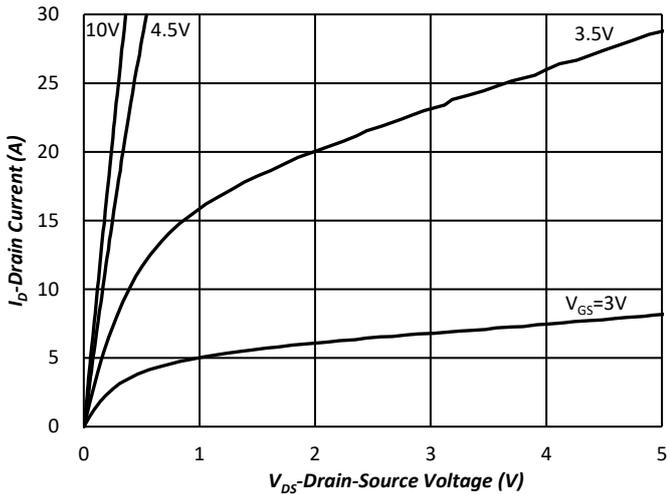


Fig.1 Typical Output Characteristics

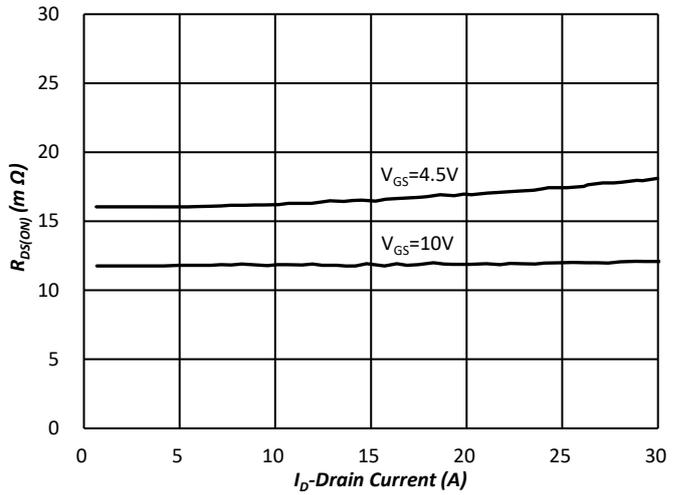


Fig.2 On-Resistance Variation with Drain Current and Gate Voltage

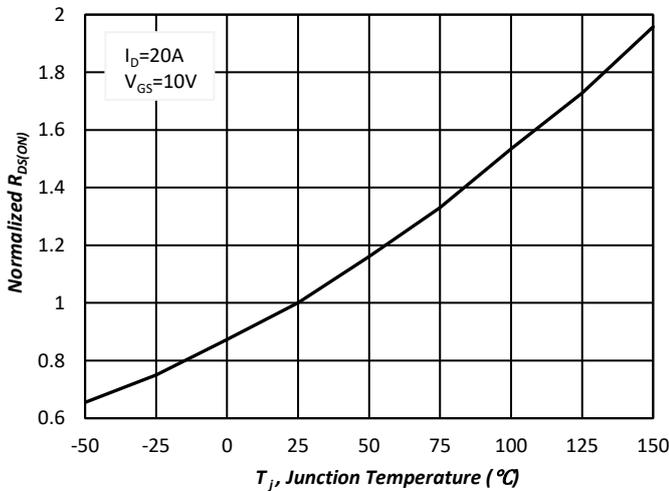


Fig.3 Normalized On-Resistance v.s. Junction Temperature

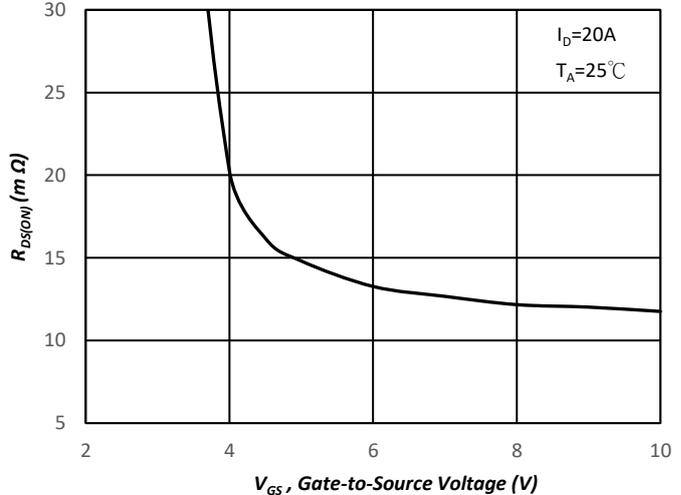


Fig.4 On-Resistance v.s. Gate Voltage

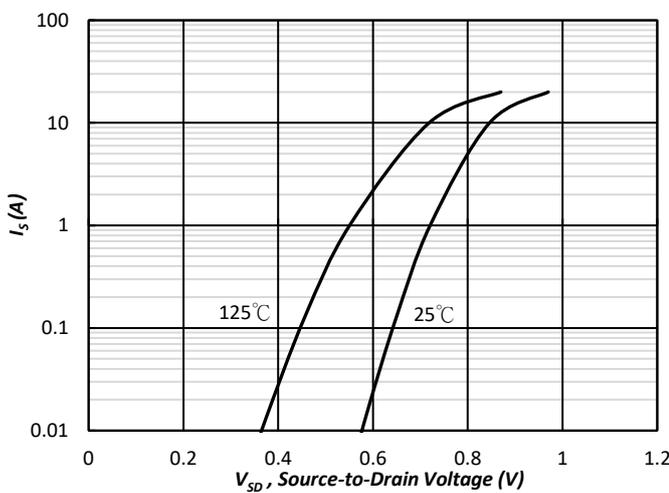


Fig.5 Forward Characteristic of Reverse Diode

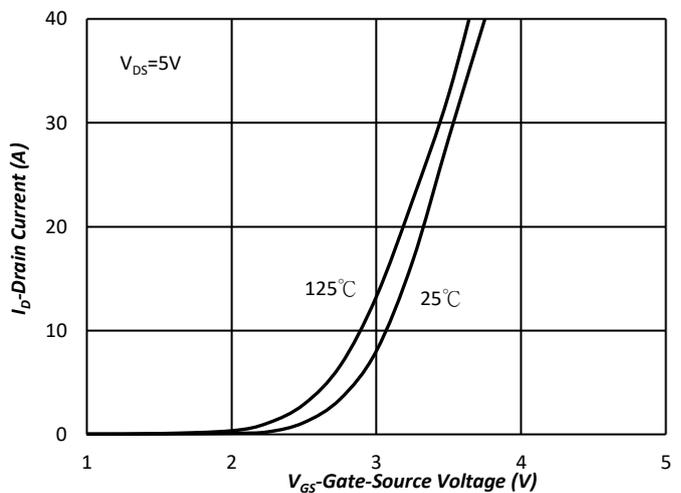


Fig.6 Transfer Characteristics

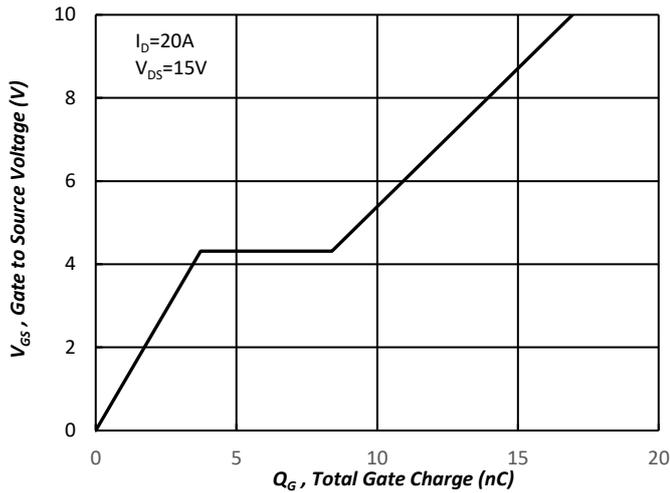


Fig.7 Gate Charge Characteristics

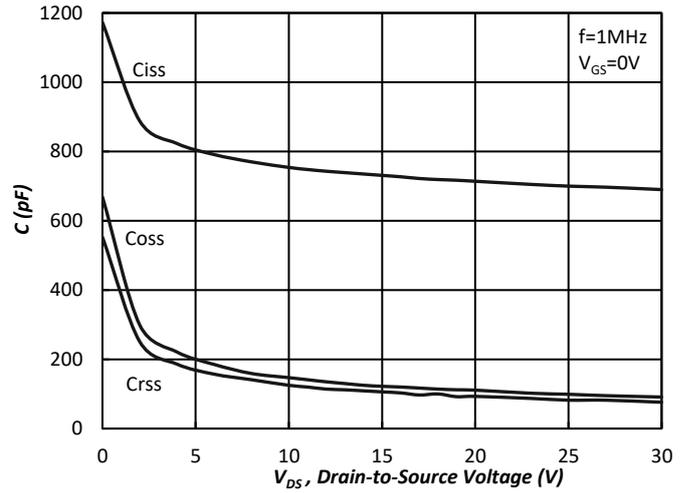


Fig.8 Typical Capacitance Characteristics

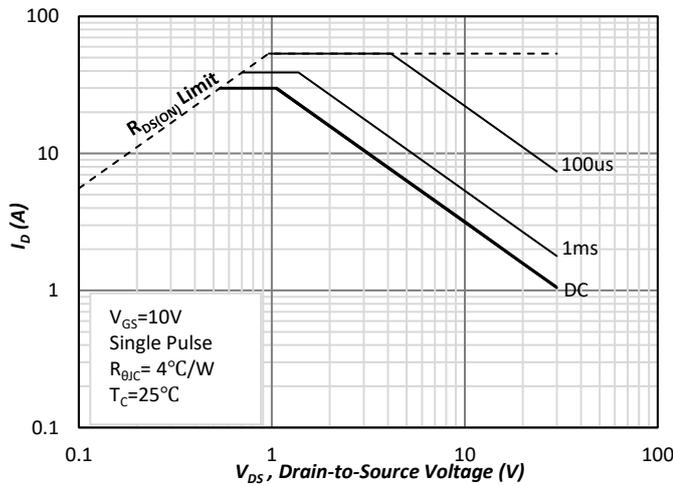


Fig.9. Maximum Safe Operating Area

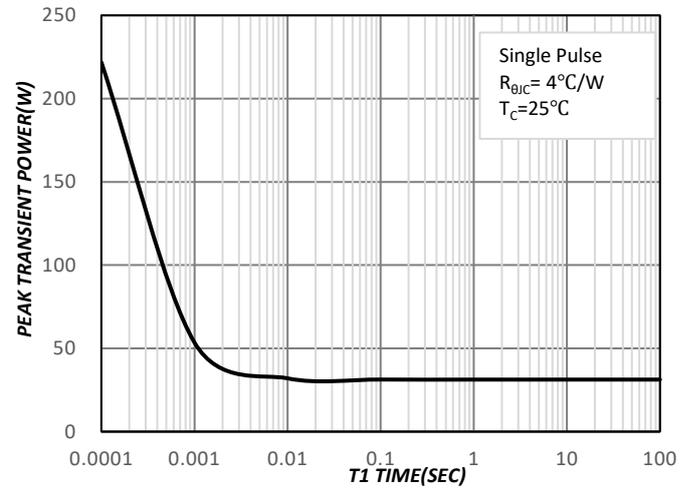


Fig.10. Single Pulse Maximum Power Dissipation

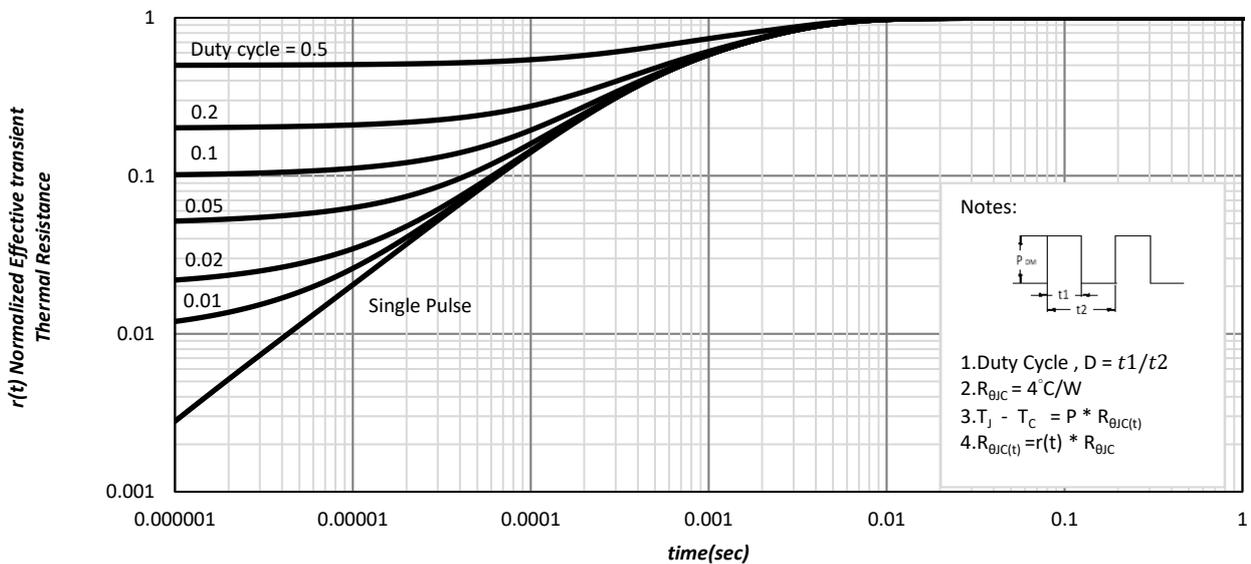


Fig.11. Effective Transient Thermal Impedance



▪ P-CH_ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage ⁴	V _{(BR)DSS}	V _{GS} = 0V, I _D = -250μA	-30			V
Gate Threshold Voltage ⁴	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250μA	-1.2	-1.6	-2.5	
Gate-Body Leakage ⁴	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V			±100	nA
Zero Gate Voltage Drain Current ⁴	I _{DSS}	V _{DS} = -30V, V _{GS} = 0V			1	μA
		V _{DS} = -30V, V _{GS} = 0V, T _J = 125 °C			25	
On-State Drain Current ¹	I _{D(ON)}	V _{DS} = -5V, V _{GS} = -10V	-44			A
Drain-Source On-State Resistance ^{1,4}	R _{DS(ON)}	V _{GS} = -10V, I _D = -15A		12	20	mΩ
		V _{GS} = -4.5V, I _D = -10A		19	30	
Forward Transconductance ¹	g _{fs}	V _{DS} = -5V, I _D = -10A		23		S
DYNAMIC						
Input Capacitance ⁵	C _{iss}	V _{GS} = 0V, V _{DS} = -15V, f = 1MHz		1255		pF
Output Capacitance ⁵	C _{oss}			215		
Reverse Transfer Capacitance ⁵	C _{rss}			175		
Gate Resistance ^{4,5}	R _g	f = 1MHz		5.6		Ω
Total Gate Charge ^{1,2,5}	Q _g (V _{GS} =10V)	V _{DS} = -15V, V _{GS} = -10V, I _D = -15A		25		nC
Total Gate Charge ^{1,2,5}	Q _g (V _{GS} =4.5V)			12		
Gate-Source Charge ^{1,2,5}	Q _{gs}			4.8		
Gate-Drain Charge ^{1,2,5}	Q _{gd}			5.3		
Turn-On Delay Time ^{1,2,5}	t _{d(on)}	V _{DS} = -15V, V _{GS} = -10V, I _D = -5A, R _g = 3Ω		6.5		nS
Rise Time ^{1,2,5}	t _r			13		
Turn-Off Delay Time ^{1,2,5}	t _{d(off)}			40		
Fall Time ^{1,2,5}	t _f			36		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS						
Continuous Current	I _S				-44	A
Pulsed Current ³	I _{SM}				-59	
Forward Voltage ^{1,4}	V _{SD}	I _F = -15A, V _{GS} = 0V			-1.2	V
Reverse Recovery Time ⁵	t _{rr}	I _F = -15A, dI _F /dt = 100A / μS		9.3		nS
Peak Reverse Recovery Current ⁵	I _{RM(REC)}			0.5		A
Reverse Recovery Charge ⁵	Q _{rr}			2.8		nC

¹Pulse test : Pulse Width ≤ 300 usec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

³Pulse width limited by maximum junction temperature.

⁴Guarantee by FT test Item

⁵Guarantee by Engineering test

EMC will review datasheet by quarter, and update new version.

▪ **P-CH_TYPICAL CHARACTERISTICS**

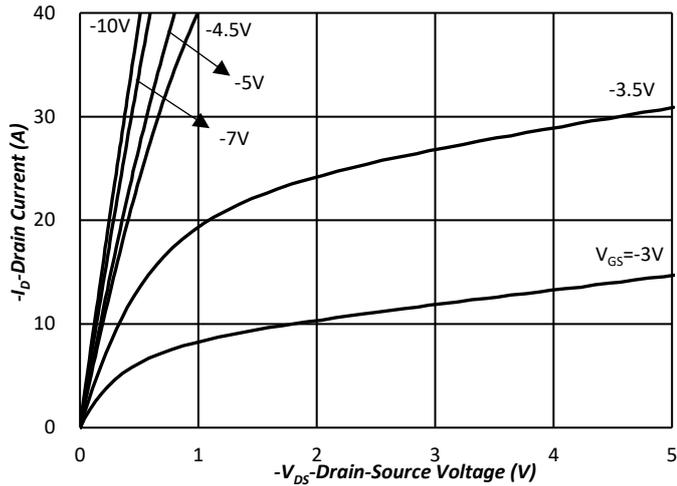


Fig.1 Typical Output Characteristics

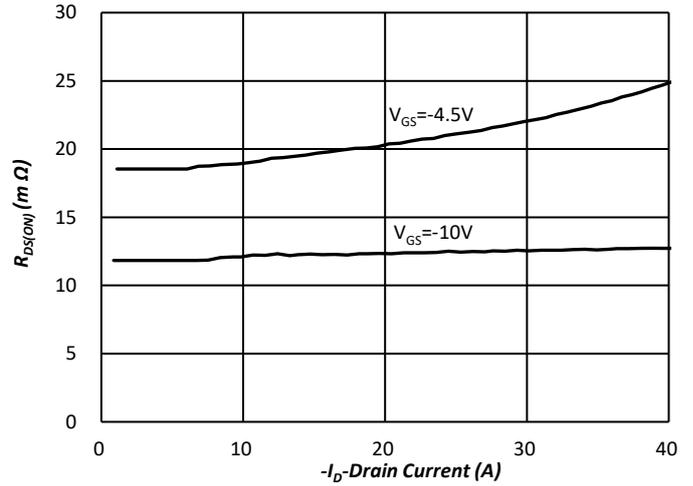


Fig.2 On-Resistance Variation with Drain Current and Gate Voltage

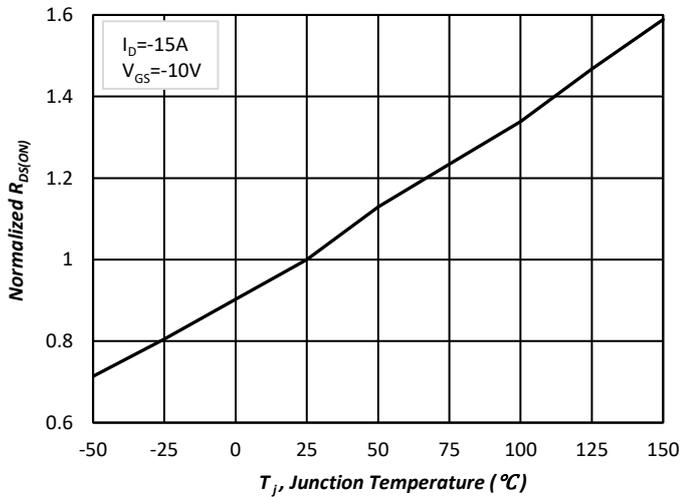


Fig.3 Normalized On-Resistance v.s. Junction Temperature

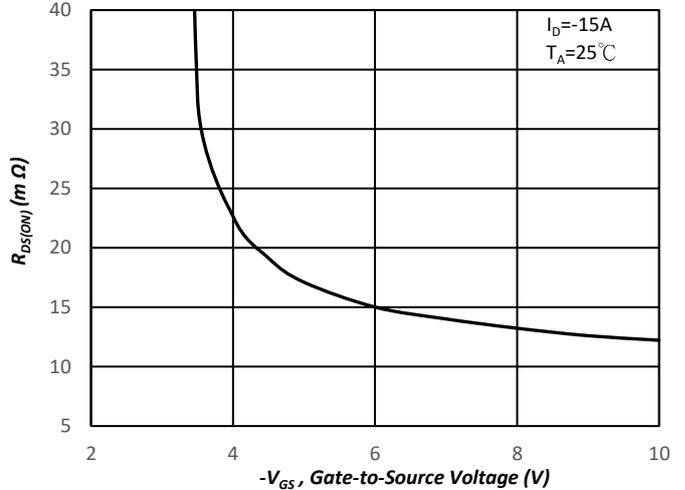


Fig.4 On-Resistance v.s. Gate Voltage

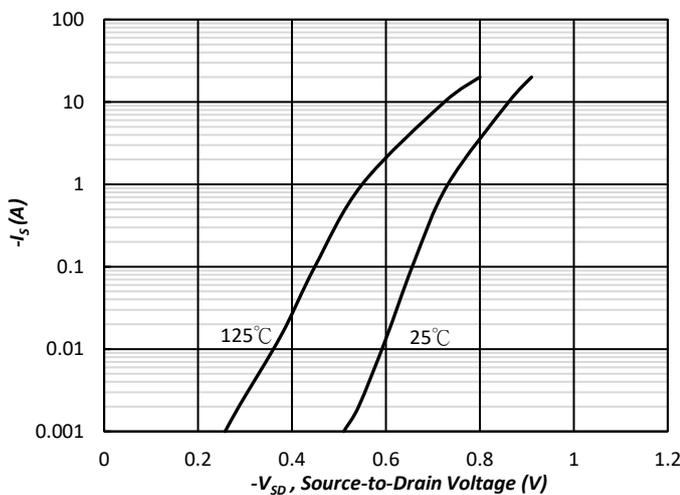


Fig.5 Forward Characteristic of Reverse Diode

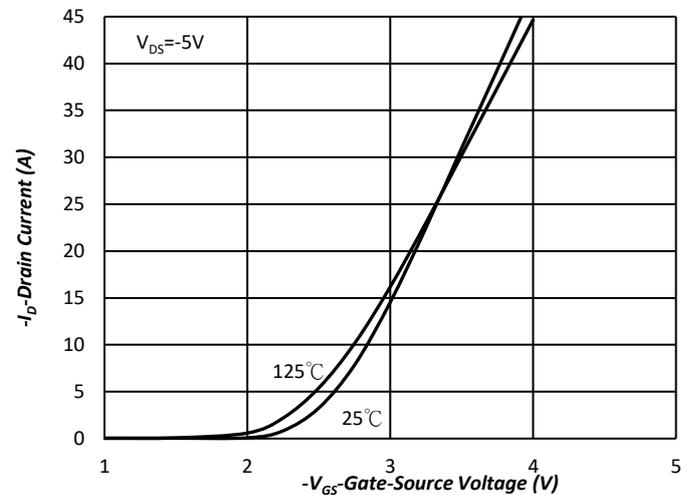


Fig.6 Transfer Characteristics

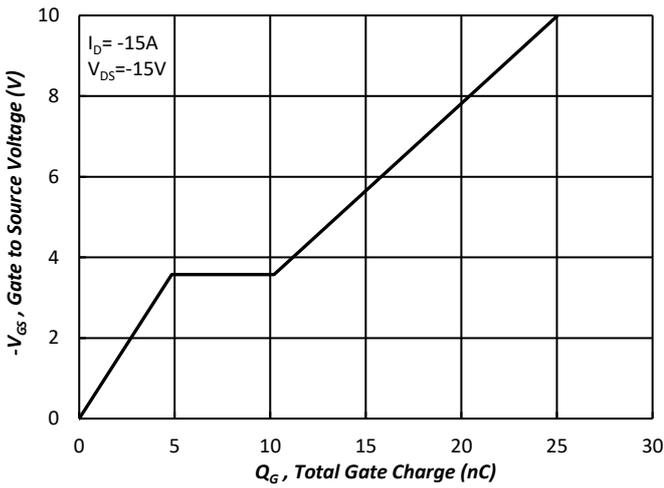


Fig.7 Gate Charge Characteristics

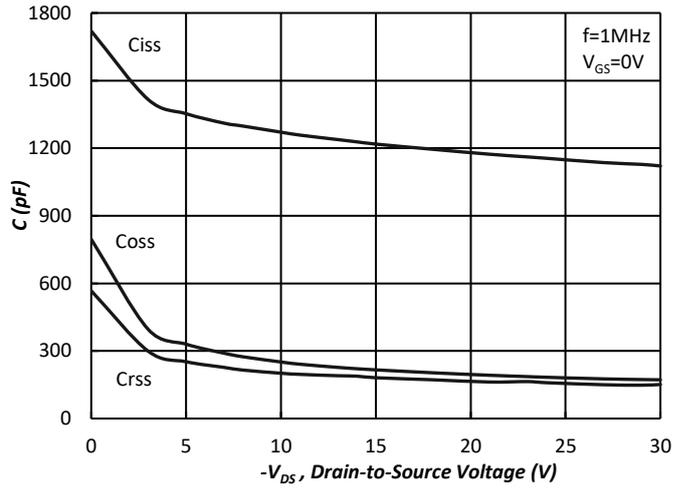


Fig.8 Typical Capacitance Characteristics

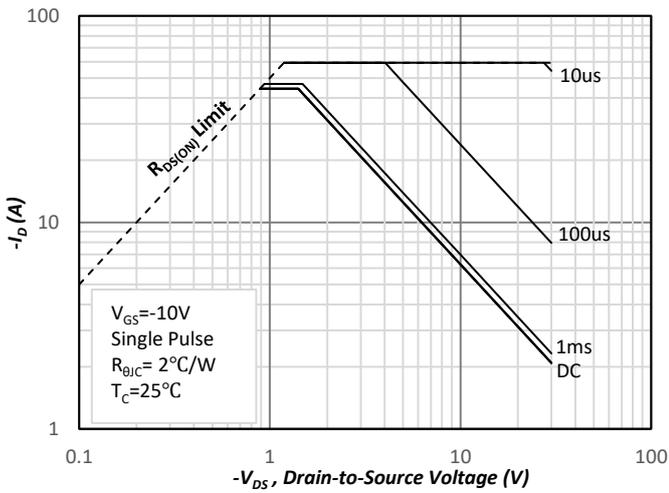


Fig.9. Maximum Safe Operating Area

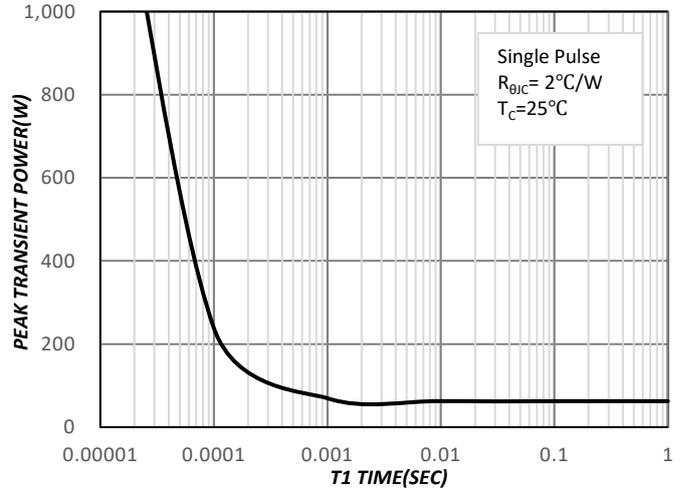


Fig.10. Single Pulse Maximum Power Dissipation

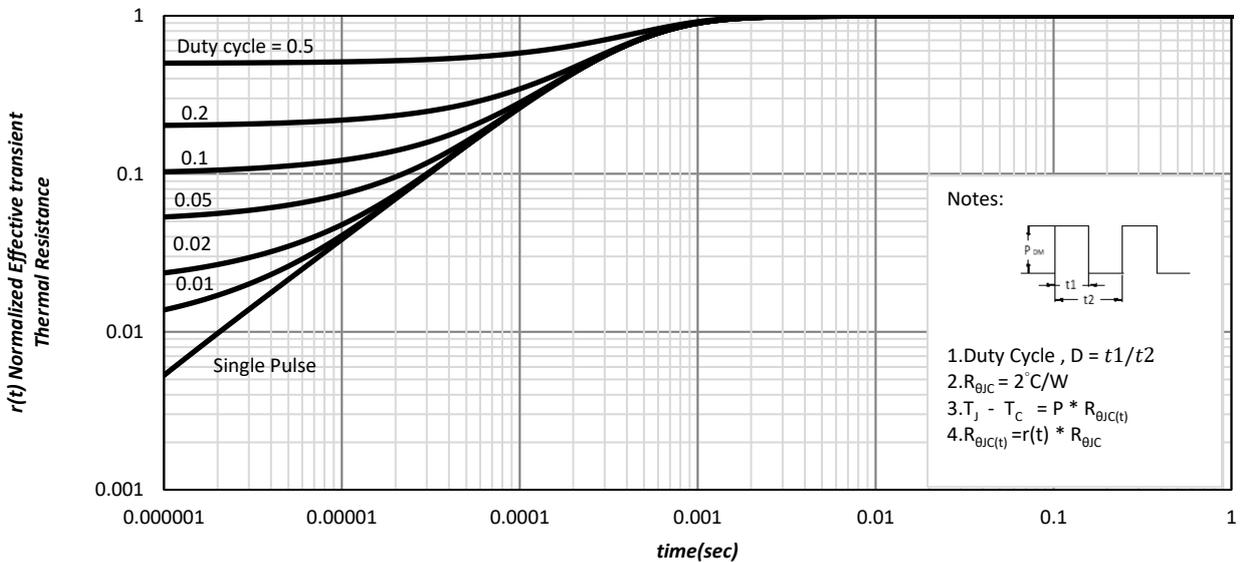


Fig.11. Effective Transient Thermal Impedance

Ordering & Marking Information:

Device Name: EMB20D03H for EDFN 5x6



B20D03: Device Name

ABCDEFGH: Date Code

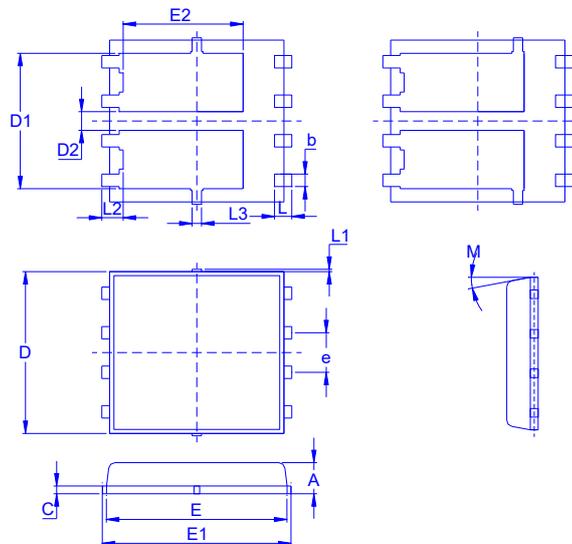
A: Assembly House

B: Year(A:2008 B:2009 C:2010....)

C: Month(A:01 B:02 C:03 D:04 E:05 F:06 G:07 H:08 I:09 J:10 K:11 L:12)

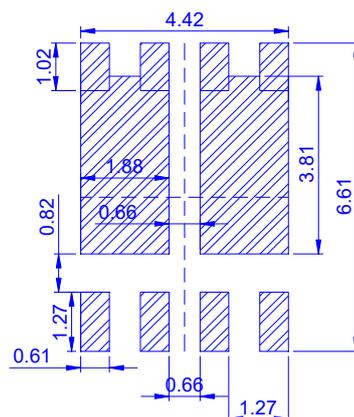
DEFG: Serial No.

Outline Drawing



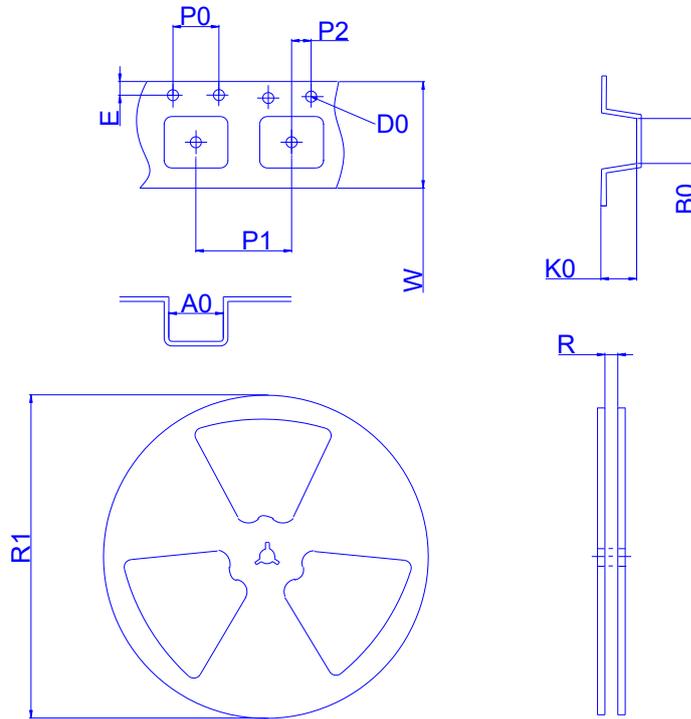
Dimension	A	b	c	D	D1	D2	E	E1	E2	e	L	L1	L2	M
Min.	0.85	0.30	0.15	4.80	3.41	0.47	5.65	5.95	3.30		0.38	-	0.38	0°
Typ.	1.01	0.40	0.20	5.00	4.01	0.67	5.75	6.05	3.43	1.27	0.55	0.09	0.48	
Max.	1.17	0.50	0.25	5.20	4.61	0.87	5.85	6.15	3.58		0.71	0.18	0.58	12°

Footprint





◆ Tape&Reel Information:2500pcs/Reel



Package	EDFN5X6
Reel	13"
Device orientation	<p>FEED DIRECTION</p>

Dimension in mm

Dimension	Carrier tape									Reel	
	A0	B0	D0	E	K0	P0	P1	P2	W	R	R1
Typ.	6.4	5.3	1.5	1.8	1.6	4	8	2	12	12.4	330
±	0.2	0.2	0.1	0.1	0.6	0.1	0.1	0.1	0.3	2	2