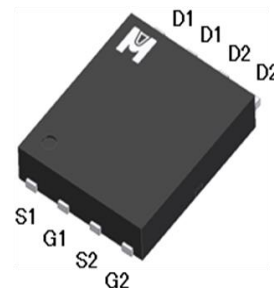
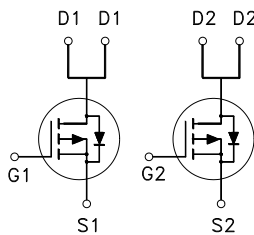


Dual P-Channel Logic Level Enhancement Mode Field Effect Transistor

Product Summary:

BV_{DSS}	-30V
$R_{DS(on)} (MAX.)$	$7.8m\Omega$
I_D	-24A



UIS, Rg 100% Tested

RoHS & Halogen Free & TSCA Compliant

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ Unless Otherwise Noted)



PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNIT
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_C = 25^\circ C$	I_D	-24	A
	$T_A = 25^\circ C (t \leq 10s)$		-14	
	$T_A = 25^\circ C (Steady-State)$		-10	
	$T_C = 100^\circ C$		-17	
Pulsed Drain Current ¹		I_{DM}	-96	
Avalanche Current		I_{AS}	-25	
Avalanche Energy	$L = 0.1mH, I_{AS} = -25A, R_G = 25\Omega$	E_{AS}	31	mJ
Repetitive Avalanche Energy ²	$L = 0.05mH$	E_{AR}	15	
Power Dissipation	$T_C = 25^\circ C$	P_D	25	W
	$T_C = 100^\circ C$		10	
Operating Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 150	$^\circ C$

100% UIS testing in condition of $V_D = 30V, L = 0.1mH, V_G = 10V, I_L = 15A$, Rated $V_{DS} = 30V$ P-CH

100% UIS testing in condition of $V_D = 30V, L = 0.1mH, V_G = 10V, I_L = 15A$, Rated $V_{DS} = 30V$ P-CH

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE		SYMBOL	TYPICAL	MAXIMUM	UNIT
Junction-to-Case		$R_{\theta JC}$		5	$^\circ C / W$
Junction-to-Ambient ³	$t \leq 10s$	$R_{\theta JA}$		45	
Junction-to-Ambient ³	Steady-State	$R_{\theta JA}$		80	

¹Pulse width limited by maximum junction temperature.

²Duty cycle < 1%

³The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz.

Copper, in a still air environment with $T_A = 25^\circ\text{C}$.

⁴Guarantee by Engineering test

ELECTRICAL CHARACTERISTICS ($T_J = 25\text{ }^{\circ}\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = -250\mu A$	-30			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu A$	-1	-1.5	-3.0	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -24V, V_{GS} = 0V$			-1	μA
		$V_{DS} = -20V, V_{GS} = 0V, T_J = 125\text{ }^{\circ}C$			-10	
On-State Drain Current ¹	$I_{D(ON)}$	$V_{DS} = -5V, V_{GS} = -10V$	-24			A
Drain-Source On-State Resistance ¹	$R_{DS(ON)}$	$V_{GS} = -10V, I_D = -20A$		6.8	7.8	m Ω
		$V_{GS} = -4.5V, I_D = -15A$		8.8	11.5	
Forward Transconductance ¹	g_{fs}	$V_{DS} = -5V, I_D = -15A$		24		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = -15V, f = 1MHz$		3270		pF
Output Capacitance	C_{oss}			483		
Reverse Transfer Capacitance	C_{rss}			477		
Gate Resistance	R_g	$V_{GS} = 15mV, V_{DS} = 0V, f = 1MHz$		3.0		Ω
Total Gate Charge ^{1,2}	Q_g	$V_{DS} = -15V, V_{GS} = -10V,$ $I_D = -20A$		60.3		nC
Gate-Source Charge ^{1,2}	Q_{gs}			6.8		
Gate-Drain Charge ^{1,2}	Q_{gd}			15		
Turn-On Delay Time ^{1,2}	$t_{d(on)}$	$V_{DS} = -15V,$ $I_D = -5A, V_{GS} = -10V, R_G = 3\Omega$		18		nS
Rise Time ^{1,2}	t_r			26		
Turn-Off Delay Time ^{1,2}	$t_{d(off)}$			22		
Fall Time ^{1,2}	t_f			75		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T _c = 25 °C)						
Continuous Current	I _S				-24	A
Pulsed Current ³	I _{SM}				-96	
Forward Voltage ¹	V _{SD}	I _F = -15A, V _{GS} = 0V			-1.2	V
Reverse Recovery Time	t _{rr}	I _F = I _S , dI _F /dt = 100A / μ S		55		nS
Reverse Recovery Charge	Q _{rr}			62		nC

¹Pulse test : Pulse Width $\leq 300\text{ }\mu\text{sec}$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

³Pulse width limited by maximum junction temperature.



Ordering & Marking Information:

Device Name: EMB07B03H for EDFN 5 x 6



B07B03: Device Name

ABCDEFG: Date Code

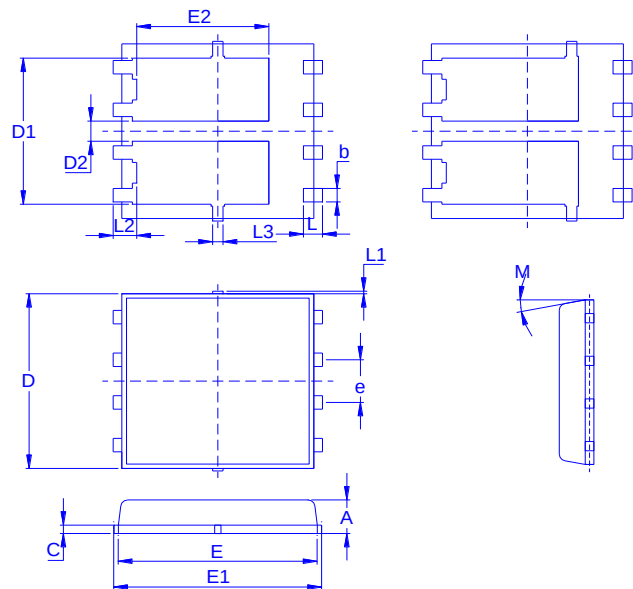
A: Assembly House

B: Year(A:2008 B:2009 C:2010....)

C: Month(A:01 B:02 C:03 D:04 E:05 F:06 G:07 H:08 I:09 J:10 K:11 L:12)

DEFG: Serial No.

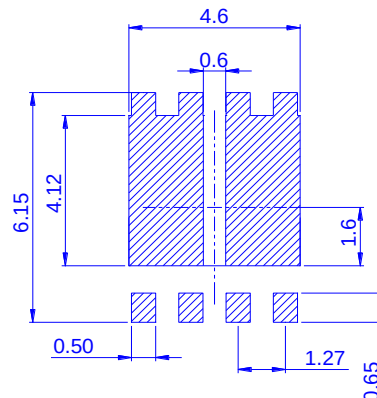
Outline Drawing

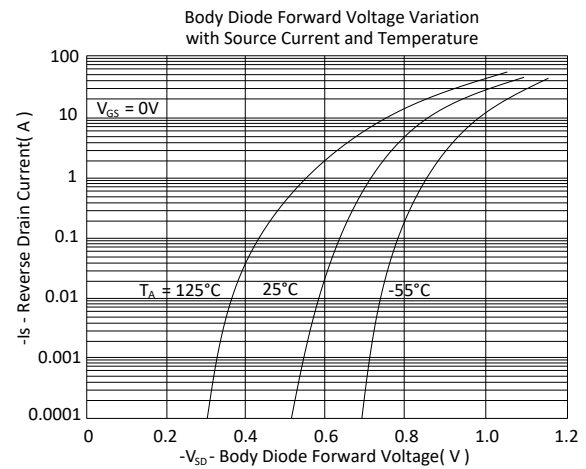
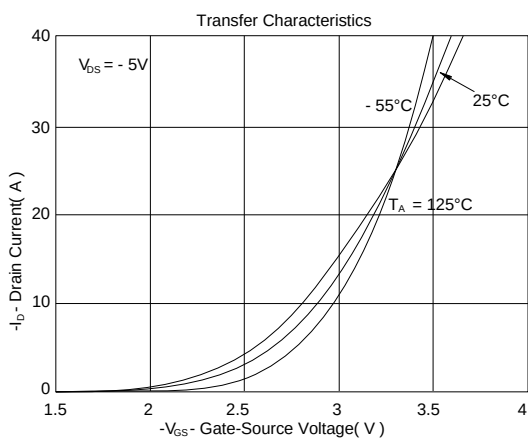
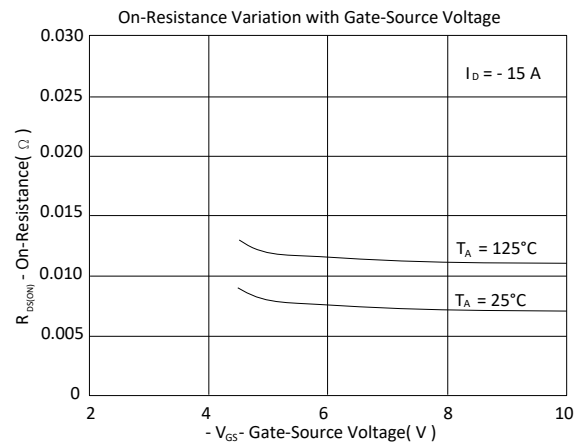
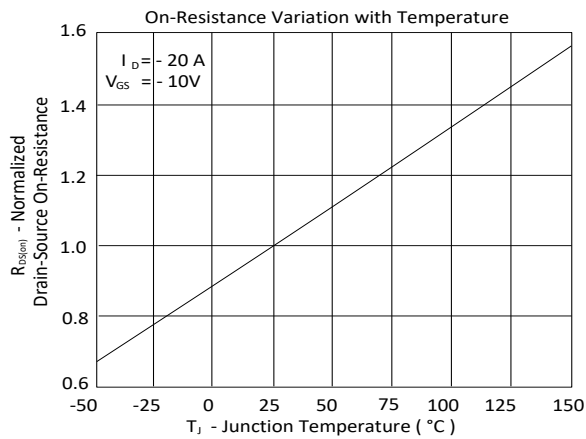
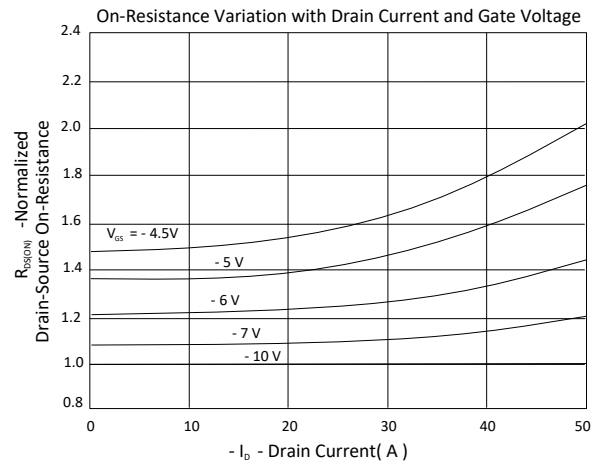
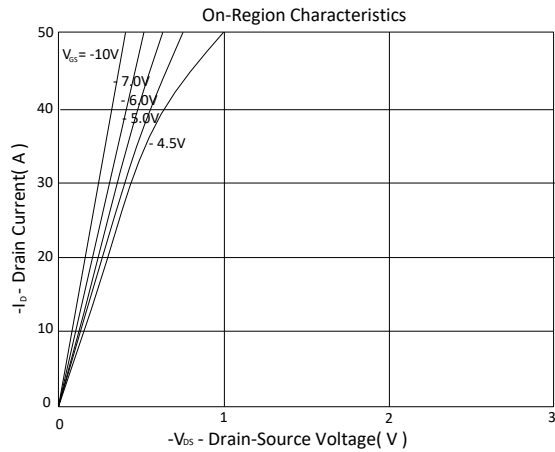


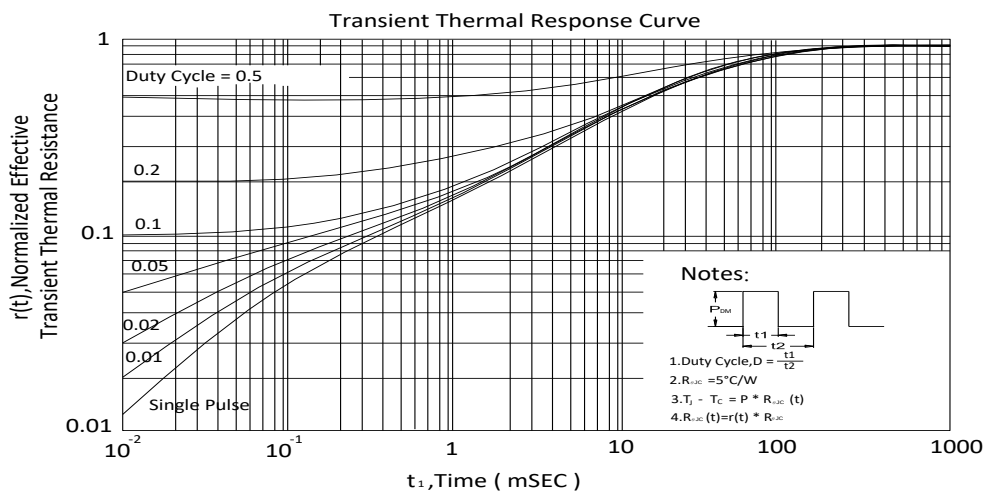
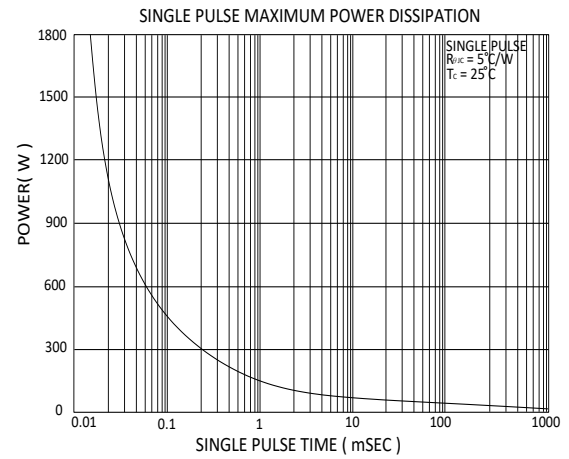
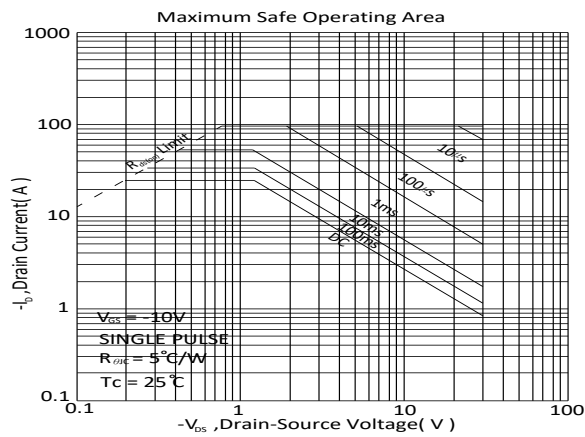
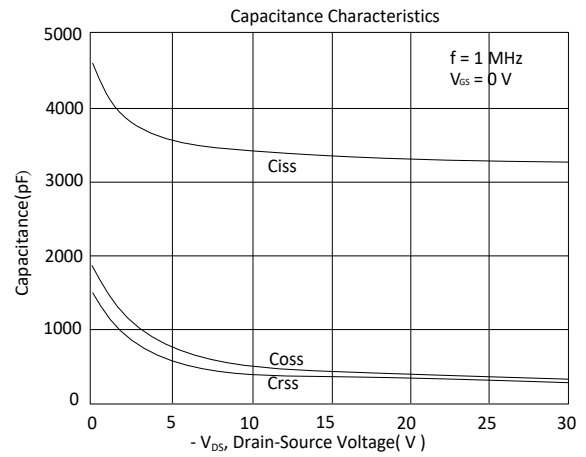
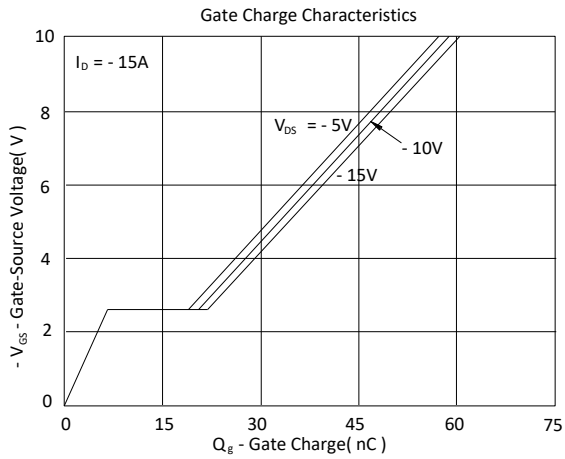
Dimension in mm

Dimension	A	b	c	D	D1	D2	E	E1	E2	e	L	L1	L2	M
Min.	0.85	0.3	0.15	4.8	3.41	0.47	5.65	5.95	3.30		0.38	0	0.38	0°
Typ.	1.01	0.4	0.2	5	4.01	0.67	5.75	6.05	3.43	1.27	0.55	0.09	0.48	
Max.	1.17	0.5	0.25	5.2	4.61	0.87	5.85	6.15	3.58		0.71	0.18	0.58	12°

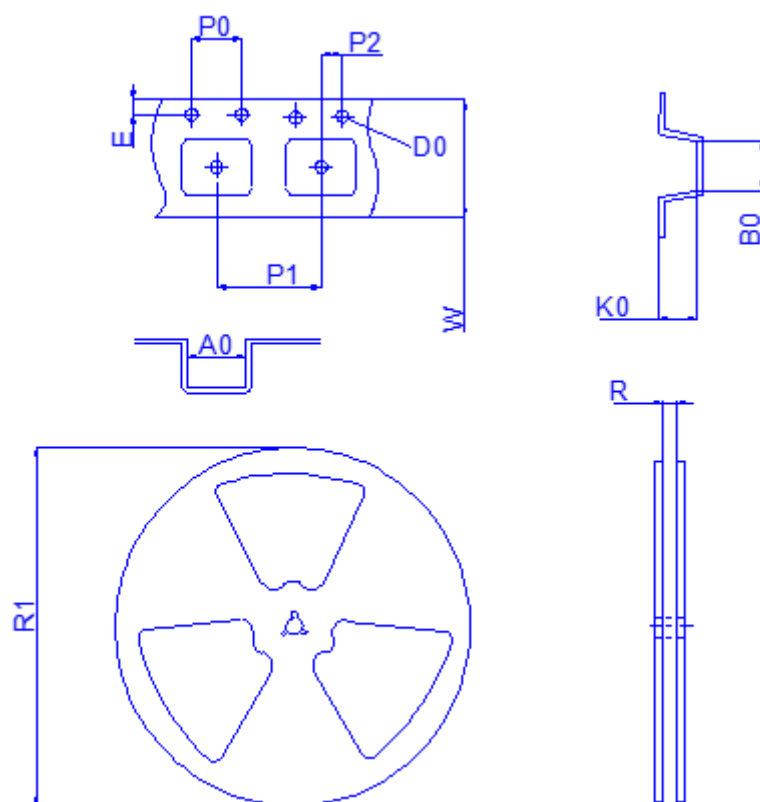
Recommended minimum pads







◆ Tape&Reel Information:2500pcs/Reel(Dimension in millimeter)



Package	EDFN5X6
Reel	13"
Device orientation	FEED DIRECTION

Dimension in mm

Dimension	Carrier tape									Reel	
	A0	B0	D0	E	K0	P0	P1	P2	W	R	R1
Typ.	6.4	5.3	1.5	1.8	1.6	4	8	2	12	12.4	330
±	0.2	0.2	0.1	0.1	0.6	0.1	0.1	0.1	0.3	2	2